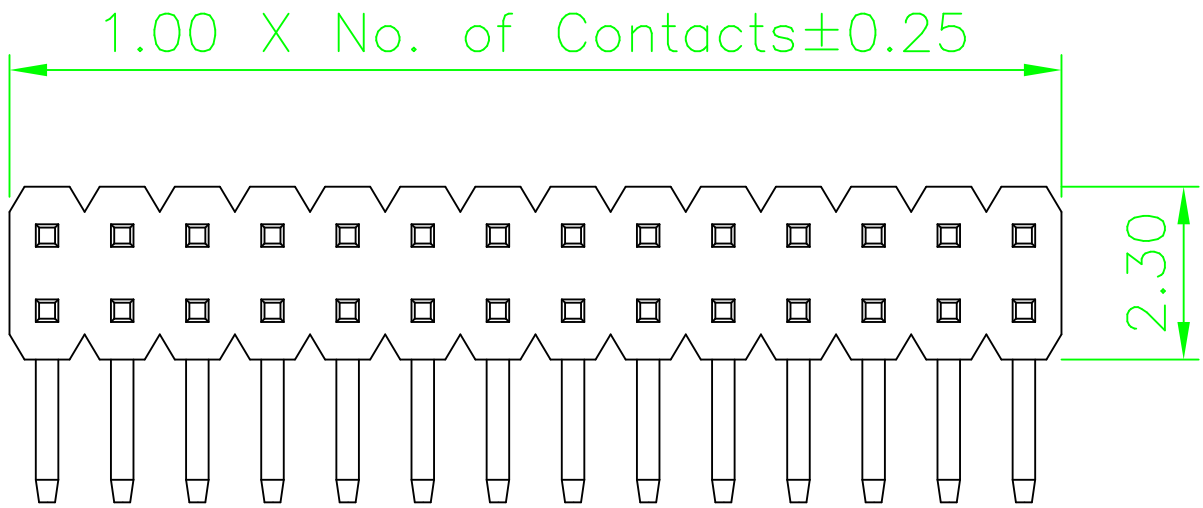
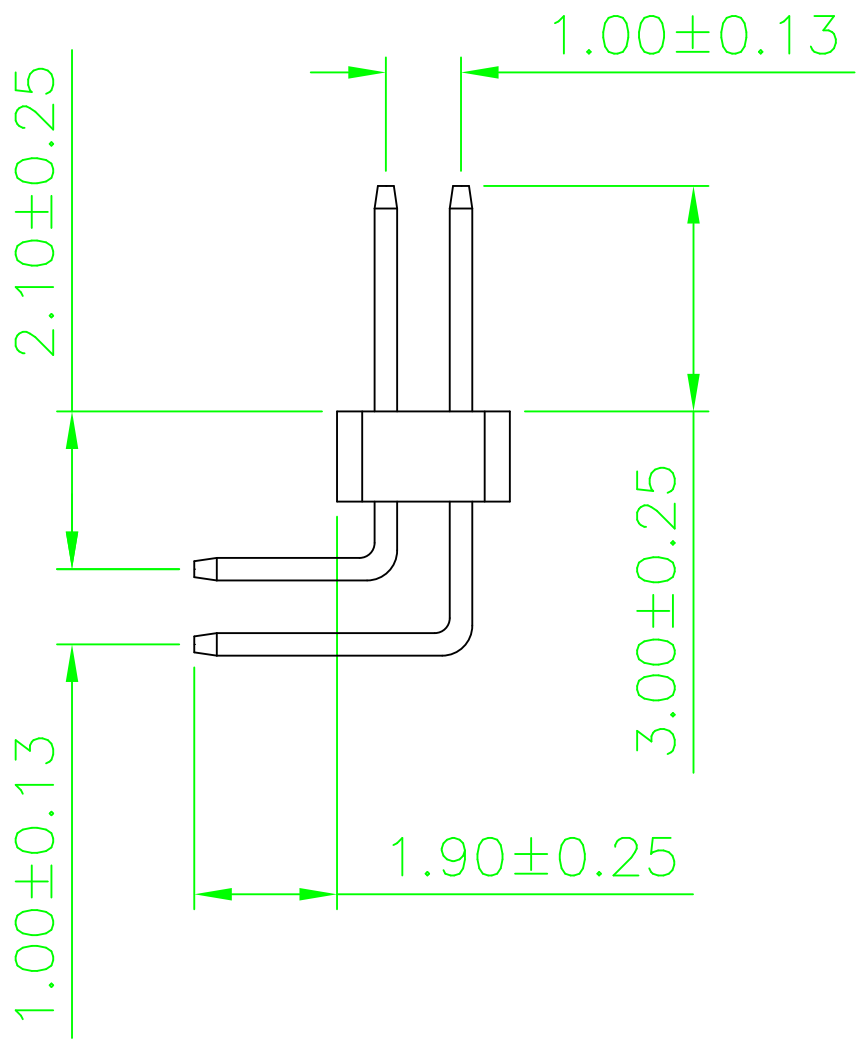
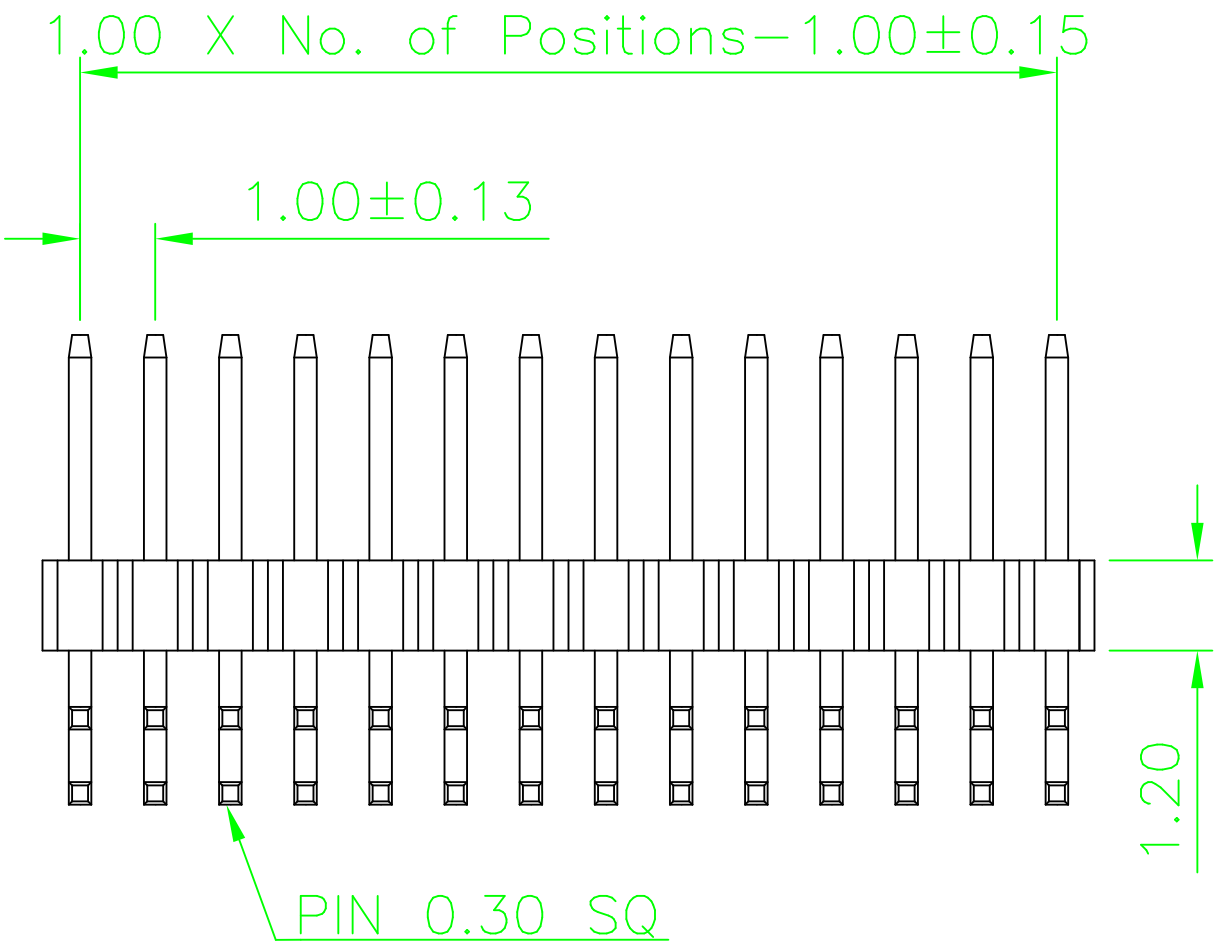
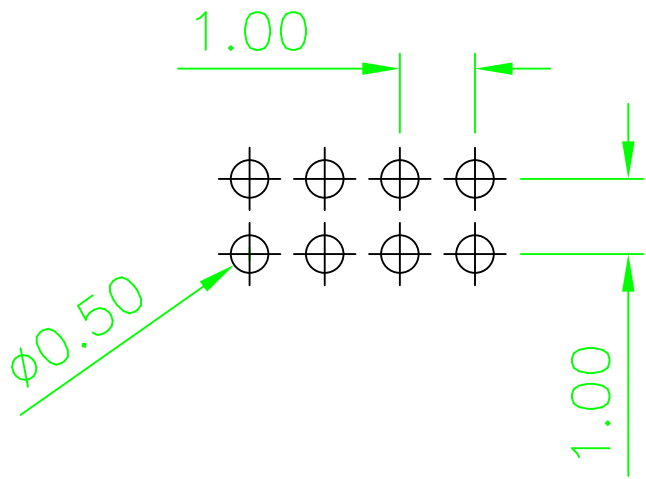


REV.	LOCAS.	DESCRIPTION	DATE	DRAWN
A		INITIAL RELEASE.	2011.11.20	XIAO PING
01		轉量產版	2011.11.20	ZHOU



Recommended P.C.Board

Hole Layout



Specifications

Current Rating: 1Amp
Insulation Resistance: 500M Ω Min.
Dielectric Withstanding: 600VAC
Contact Resistance: 20m Ω Max.
Operating Temperature: -40°C to $+105^{\circ}\text{C}$
Max Process Temp: 230°C for 30~60 sec.
(260°C for 10 seconds)

Materials

Contact Material: Phosphor Bronze
Insulator Material: PA6T, UL 94V-0

All materials must be Geron materials

Ordering Information

GRA32 - XX X W01

No. of Pins per Row
02~50

Contact Plating
1=Tin
2=Gold Flash
Duplex plating
5=Gold/TIN

GERON ELECTRONIC CO.,LTD

GENERAL TOLERANCE

$X^{\circ} \pm 5^{\circ}$	$X. \pm 0.30$
$.X^{\circ} \pm 3^{\circ}$	$.X \pm 0.25$
$.XX^{\circ} \pm 2^{\circ}$	$.XX \pm 0.15$
$.XXX^{\circ} \pm 1.0^{\circ}$	$.XXX \pm 0.10$

DESIGN: ZHOU

DATE: 2011.11.20

DWG. NO.

TITLE.

CHECKED:

DATE:

GRA32-W01

AUDITED:

DATE:

PART NO.

APPROVED:

DATE:

GRA32-XX-XX-W01

1.00*1.00 BORAD
SPACER DUAL ROWS
BASE 1.20H

REV.	UNIT.	SCALE.	PAGE.	
01	mm	5:1	1/1	